

IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Claims 1 and 4-5 have been amended and claims 6-12 have been added as follows:

Listing of Claims:

Claim 1 (currently amended): A FED control circuit for controlling an electrode voltage of a field emission display which includes a plurality of cathode electrodes and gate electrodes, both of which being arranged in a lattice shape; emitters, each of which being arranged at an intersection point of said cathode electrode and said gate electrode; fluorescent materials and anode electrodes, both of which being disposed opposing to said cathode electrode, said FED control circuit comprising:

a cathode voltage control unit for controlling said cathode electrode so that electron emission from said cathode electrode is uniform; and

a gate electrode driving unit for changing a gate electrode voltage in response to a video signal.

Claim 2 (original): The FED control circuit according to claim 1, wherein said cathode voltage control unit charges a capacitor by a constant current and determines a cathode voltage of each pixel by controlling charging time.

Claim 3 (original): The FED control circuit according to claim 2, wherein said charging time of said capacitor is controlled by pulse width.

Claim 4 (currently amended): The FED control circuit according to ~~any one of claims 1 to 3~~ claim 1, wherein said gate electrode driving unit performs ON/OFF control of said gate electrode by complementary connection.

Claim 5 (currently amended): The FED circuit according to ~~any one of claims 1 to 4~~ claim 1, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 6 (new): The FED control circuit according to claim 2, wherein said gate electrode driving unit performs ON/OFF control of said gate electrode by complementary connection.

Claim 7 (new): The FED control circuit according to claim 3, wherein said gate electrode driving unit performs ON/OFF control of said gate electrode by complementary connection.

Claim 8 (new): The FED circuit according to claim 2, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 9 (new): The FED circuit according to claim 3, further comprising a characteristics

correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 10 (new): The FED circuit according to claim 4, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 11 (new): The FED circuit according to claim 6, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.

Claim 12 (new): The FED circuit according to claim 7, further comprising a characteristics correction unit which continuously corrects variation for every said gate electrode by a data table.